Logic Unit for Programmable Operation LUPO

- Can use LUPO as various logic circuit.
- Lupo is FPGA and VME interface module.
- The FPGA on board Lupo can renew a user optionally.
- The FPGA is using XC3S500E. (500K Gate, 360K RAM)
- 4 NIM input signal
- 4 NIM output signals
- 16 LVDS signal input and output
- 8 LED Can synchronize with a clock of several A3XXX.
- A24/34 Data transfer , 1 interactive.
- VME 6U 1span

Applications

There are the following examples in the application of LUPO by Dr.Baba of RIKEN.

➢ Time Stump Module application
➢Output Register Module application
➢Trigger Select Module application
➢Input & Output & Interrupt Register Module application

- This product is developed for the purpose of using it in Japan.
- When you plan use except Japan, The duty in accordance with the law of the countries concerned government produces you.

Please perform an inquiry of more information at the following.

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